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APPLICATION NO.	FILING DATE			FIRST NAMED INVENTOR			АТТО	RNEY DOCKET NO.	CONFIRMATION NO.
10/600,999	06/20/2003		William H. Robertson CA7010223001					5787	
TITLE OF INVENTION LOCATED RESOURCES		YSTEM F	FOR FACILIT	ATING ELECTRON	NIC (	CIRCUIT AND C	CHIP D	DESIGN USING REM	MOTELY
APPLN. TYPE	SMALL ENTITY	SMALL ENTITY ISSUE FEE DU		PUBLICATION FEE		PREV. PAID ISSU	E FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	NO \$1440		\$300		\$0		\$1740	02/05/2008
EXAMINER			T UNIT	CLASS-SUBCLASS		,			
WHITMORE, STACY 2825				716-001000	-001000				
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(A) NAME OF ASSIC				(B) RESIDENCE: (			COUNT	RY)	
Cadence Design Systems, Inc. San Jose, California									
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Typed or printed name	Erich C. Tze	ou				Registration 1	No	56,927	<del></del>
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